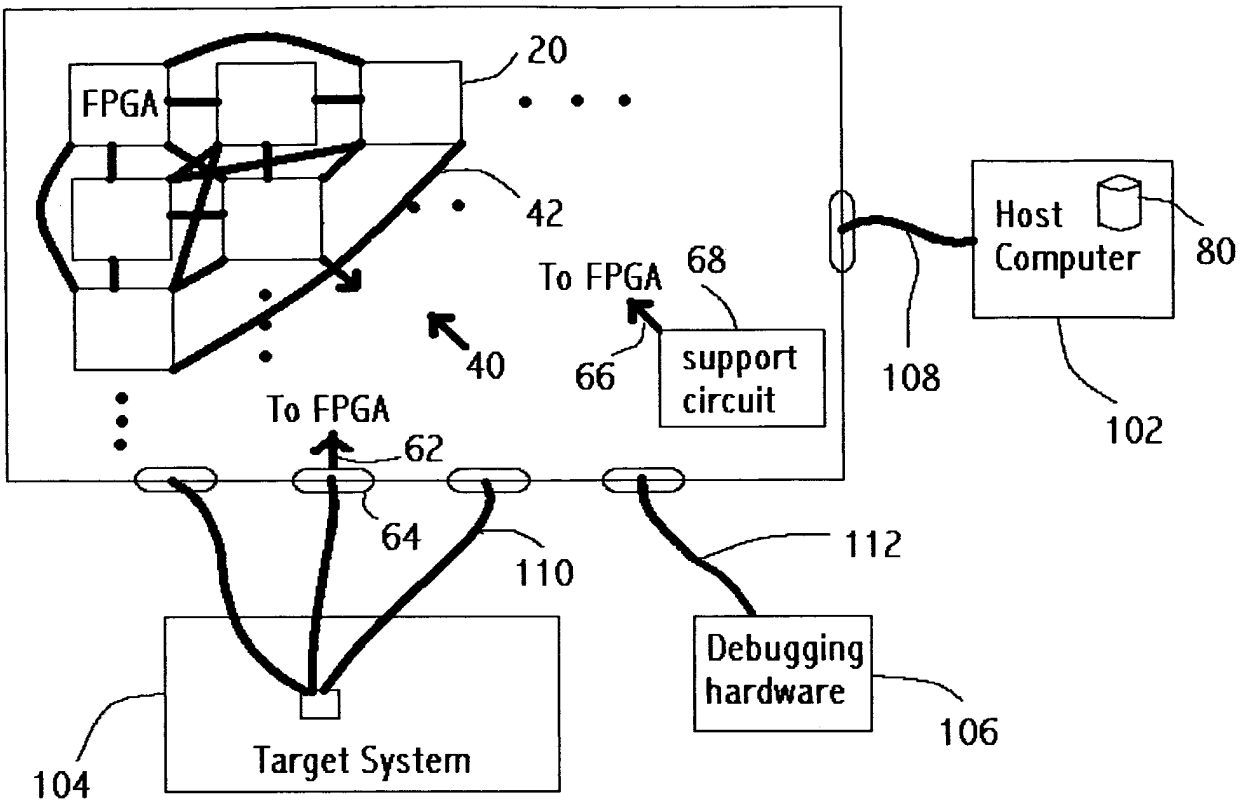
FPGA
20
42
To FPGA
68
40
66
support circuit
Host Computer
80
108
102
To FPGA
62
64
110
112
Debugging hardware
106
Target System
104

Figure 1

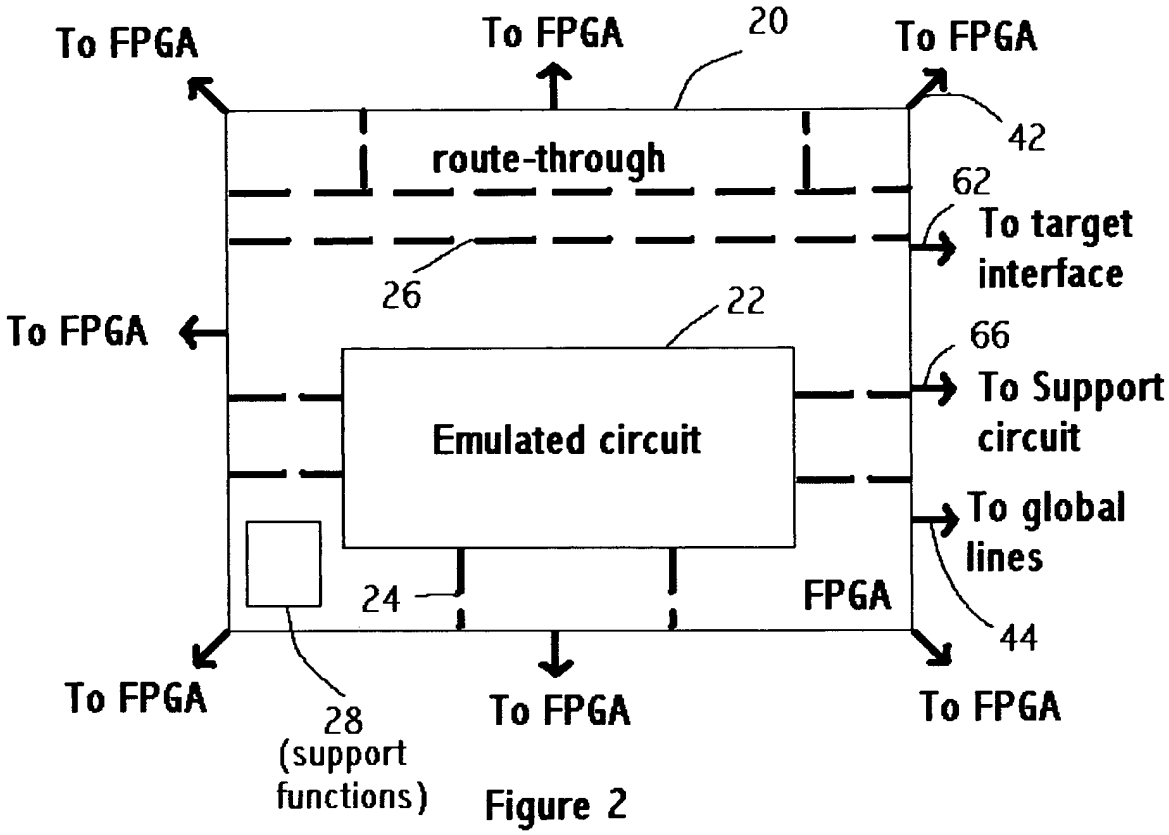
To FPGA
To FPGA
20
To FPGA
42
route-through
62
To target interface
26
22
To FPGA
66
To Support circuit
Emulated circuit
To global lines
24
FPGA
44
To FPGA
To FPGA
To FPGA
28 (support functions)

Figure 2

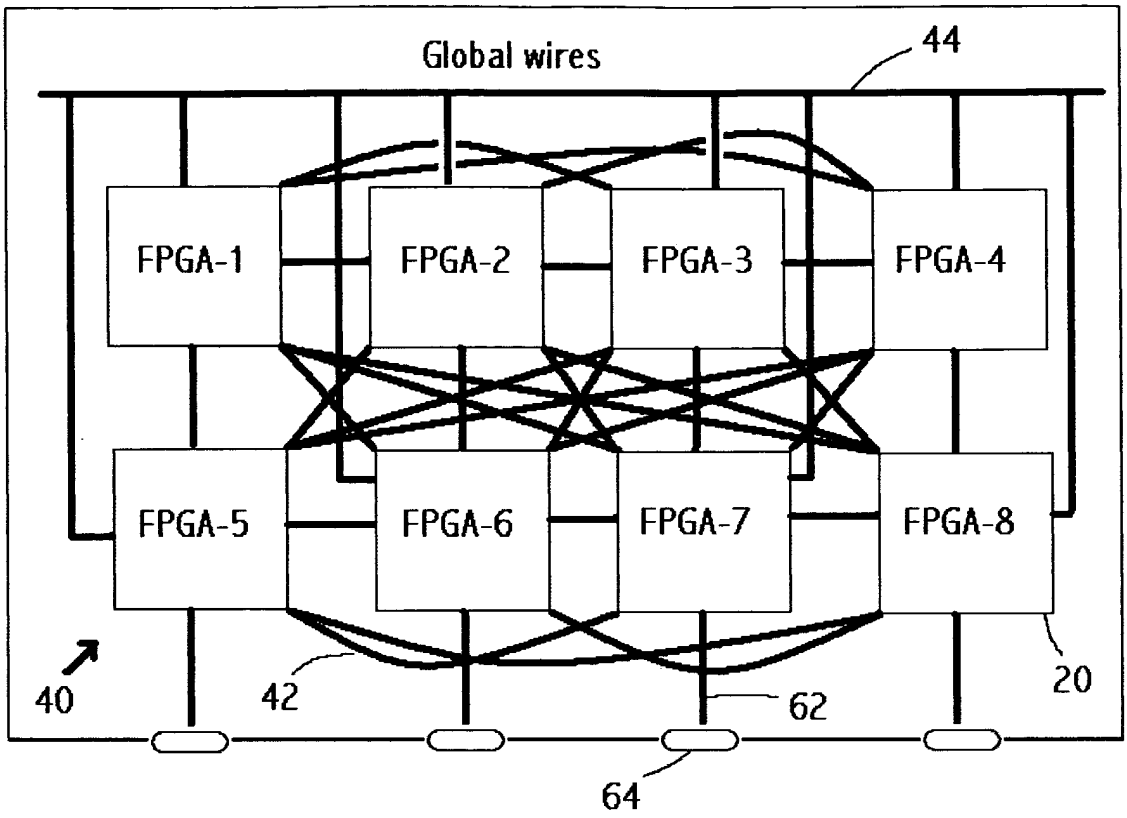
Global wires
44
FPGA-1
FPGA-2
FPGA-3
FPGA-4
FPGA-5
FPGA-6
FPGA-7
FPGA-8
40
42
62
20
64

Figure 3

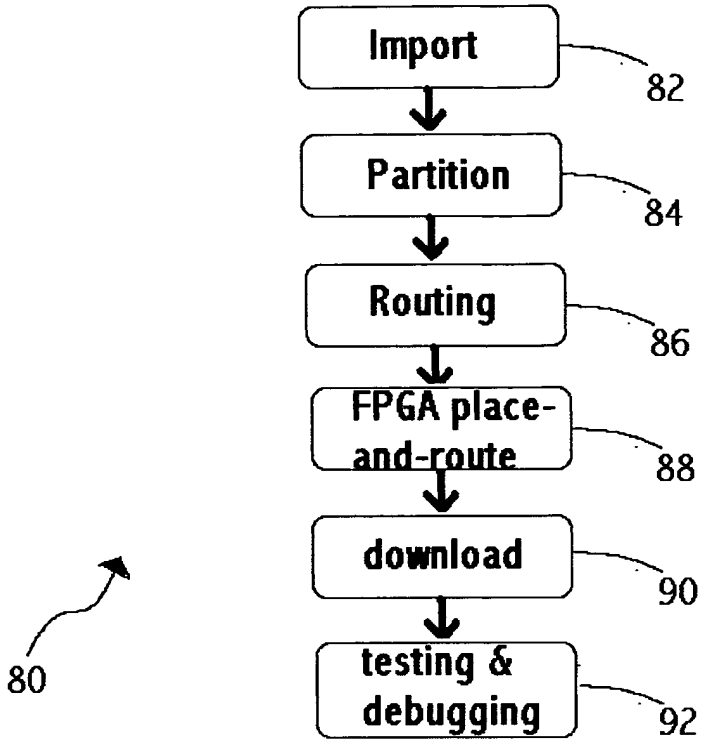
Import
82
Partition
84
Routing
86
FPGA place-and-route
88
download
90
testing & debugging
92
80

Figure 4